

10981866-1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:		\dashv
) Gupta et al.)	Examiner:	Makhdoom, S.
) Serial No.: 09/378,596)	Art Unit:	2123
Filing Date: August 20, 1999)		
For: AUTOMATIC DESIGN OF) PROCESSOR DATAPATHS)		RECEIVED APR 1 7 2003 Technology Center 2100

AMENDMENT AND RESPONSE TO OFFICE ACTION

Hon. Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

In response to the Office Action mailed February 10, 2002, Applicants respectfully request reconsideration of the above referenced patent application. Please amend the above identified application as follows and consider the following remarks for allowance of the above identified patent application.

IN THE CLAIMS

CLEAN (UNMARKED) COPY OF AMENDED CLAIMS

Please enter the following amendment:

16. (Amended) A method for automatic synthesis of a register file and functional unit-

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Serial No.: 09/378,596

Group Art Unit: 2123

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